

LS-SM5510-80C

10.3Gbps SFP+ Transceiver, Single Mode, 80km Reach

Product Features

- Supports up to 10.7Gbps bit rates
- Hot-pluggable SFP+ footprint
- > 1550nm Cooled EML laser and APD photodiode, Up to 80km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:
 - Standard:0 to +70°C

Applications

- 10Gbps Optical systems
- 10GBASE-ZR at 10.3125Gbps
- ➤ 10GBASE-ZW at 9.953Gbps
- LTE systems
- Other Optical links

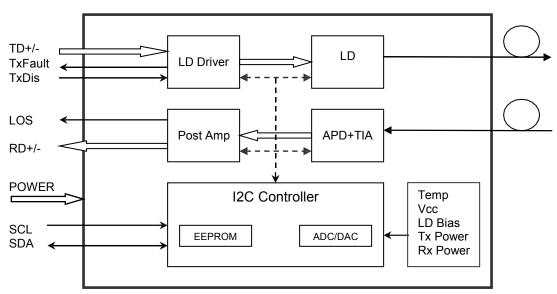


Description

The SFP+ transceivers are high performance, cost effective modules supporting data rate of 10Gbps and 80km transmission distance with SMF.

The transceiver consists of three sections: a Cooled EML laser transmitter, a APD photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.



Transceiver functional diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Тс	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			600	mA
Data Rate		1.0	10.3	10.7	Gbps



Optical and Electrical Characteristics

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							1
Centre Wavel	Centre Wavelength		1530	1550	1565	nm	
Spectral Widtl	h (-20dB)	Δλ			1	nm	
Side-Mode Su	ppression Ratio	SMSR	30	-		dB	
Average Outp	ut Power	Pout	0		+4	dBm	1
Extinction Rat	io	ER	6.0			dB	
Data Input Sw	ing Differential	VIN	180		850	mV	2
Input Differen	tial Impedance	ZIN	90	100	110	Ω	
	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Rec	eiver		I	ı
Centre Wavelength		λc	1260		1600	nm	
Receiver Sens	itivity				-23	dBm	3
Receiver Over	load		-7			dBm	3
LOS De-Assert		LOSD			-24	dBm	
LOS Assert		LOSA	-35			dBm	
LOS Hysteresis			0.5		4	dB	
Data Output S	wing Differential	Vout	300		900	mV	4
		High	2.0		Vcc	V	
LOS		Low			0.8	V	

Notes:

The optical power is launched into SMF.

PECL input, internally AC-coupled and terminated.

Measured with a PRBS 231-1 test pattern @10312Mbps, BER ≤1×10-12.

Internally AC-coupled.



Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	VH	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-1 to +4	dBm	±3dB	Internal
RX Power	-23 to -6	dBm	±3dB	Internal

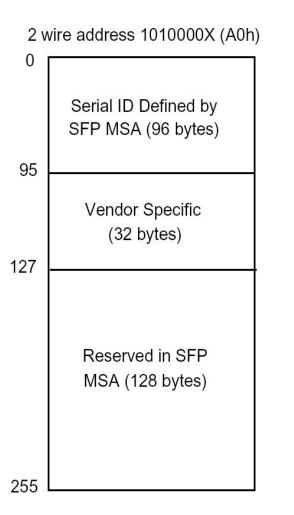


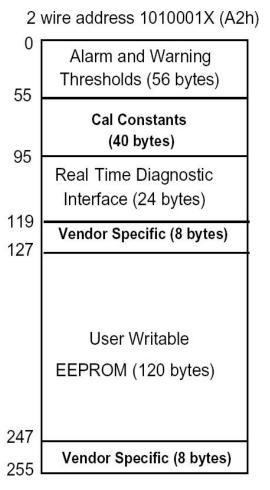
Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

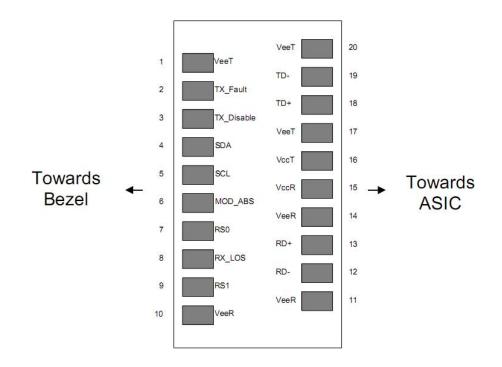
The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.





Pin Descriptions



Signal Name	Description	Plug Seq.	Notes
VEET	Transmitter Ground	1	
TX FAULT	Transmitter Fault Indication	3	Note 1
TX DISABLE	Transmitter Disable	3	Note 2
SDA	SDA Serial Data Signal	3	
SCL	SCL Serial Clock Signal	3	
MOD_ABS	Module Absent. Grounded within the module	3	
RS0	Not Connected	3	
LOS	Loss of Signal	3	Note 3
RS1	Not Connected	3	
VEER	Receiver ground	1	
VEER	Receiver ground	1	
RD-	Inv. Received Data Out	3	Note 4
RD+	Received Data Out	3	Note 4
VEER	Receiver ground	1	
VCCR	Receiver Power Supply	2	
VCCT	Transmitter Power Supply	2	
	VEET TX FAULT TX DISABLE SDA SCL MOD_ABS RSO LOS RS1 VEER VEER RD- RD+ VEER VCCR	VEET Transmitter Ground TX FAULT Transmitter Fault Indication TX DISABLE Transmitter Disable SDA SDA Serial Data Signal SCL SCL Serial Clock Signal MOD_ABS Module Absent. Grounded within the module RSO Not Connected LOS Loss of Signal RS1 Not Connected VEER Receiver ground VEER Receiver ground RD- Inv. Received Data Out RD+ Receiver Data Out VEER Receiver ground VCCR Receiver Power Supply	VEETTransmitter Ground1TX FAULTTransmitter Fault Indication3TX DISABLETransmitter Disable3SDASDA Serial Data Signal3SCLSCL Serial Clock Signal3MOD_ABSModule Absent. Grounded within the module3RSONot Connected3LOSLoss of Signal3RS1Not Connected3VEERReceiver ground1VEERReceiver ground1RD-Inv. Received Data Out3RD+Receiver ground1VEERReceiver ground1VEERReceiver ground1VEERReceiver ground1VEERReceiver ground1VEERReceiver ground1VCCRReceiver Power Supply2

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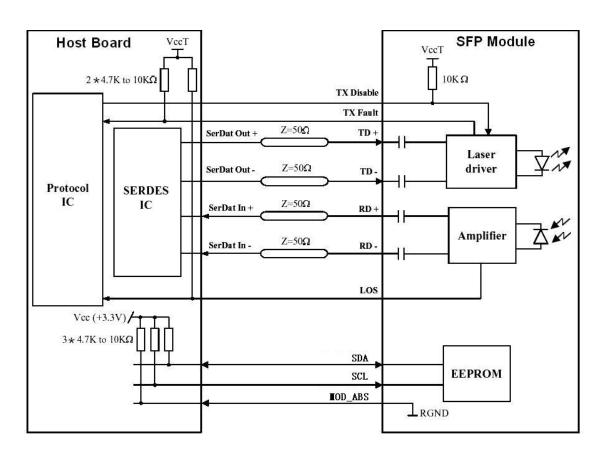
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	VEET	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

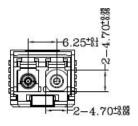
- 1. TX Fault is an open collector output, which should be pulled up with a $4.7k^{\sim}10k\Omega$ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2. Laser output disabled on TDIS > 2.0V or open, enabled on TDIS < 0.8V.
- 3. LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4. RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5. TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

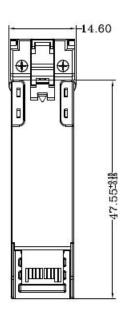
Recommended Interface Circuit

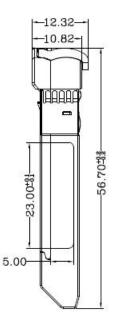


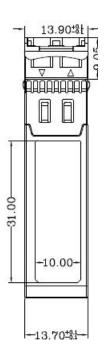


Mechanical Dimensions









Ordering information

Part Number	Product Description					
LS-SM5510-80C	1550nm,	10.3Gbps,	LC,	80km,	0°C~+70°C,	with DDM